

# Design of a New-style and High-stability LD Drive Power Supply

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**Abstract**—This paper describes the design of a new-style and high-stability semiconductor laser diode(LD) linear drive power supply, which is able to drive 100mW to 1.8W laser diodes and the output current can be continuously adjustable from 0A to 2.5A. The current stability obtained in the LD driver circuit is superior to 0.3%. In the design we have innovative ideas of the soft-start circuit, the current-limiting circuit with special rated-current setting function, the surge ripple prevention and filter circuit, especially enhance the stability and security of the power drive system. Such design will be found wide applications in the fields of teaching, scientific research and production.

**Keywords**- semiconductor laser diode; power supply; stability

## I. INTRODUCTION

Semiconductor lasers have not only a high degree of monochromaticity, coherence, directionality, and brightness like general lasers, but also have advantages such as a small size, light weight, direct modulation, and low price. So they have been more and more widely used in national defense, scientific research, medical, optical communications and other fields [1, 2].

LD is the current drive device, the operating life and work characteristics depend largely on the performance of the drive power supply [3]. Because semiconductor laser is a device of high power density and with high quantum efficiency, the capability of suffer from electric shocks is very poor, micro-current fluctuation will cause a great change of optical power and device parameters(such as laser wavelength, noise performance), and these changes directly endangering the safe use of LD. So in practice application, the performance and security requirements of the drive power are highly demanded. Weakness in design of drive power will do harm to LD or even damage it, therefore, design a drive power fit for the LD technical requirements with stable performance and reliable operation is quite necessary[4,5,6,7]. In our design of the drive power, we focus on the safe and effective protection of the LD, such as soft-start circuit, surge prevention circuit, and current-limiting circuit[8]. We use a 635nm LD whose type is G0331 in experimental testing, the result can meet the LD requirements, and it can work with high security and stability.

## II. SYSTEM OVERVIEW

### A. Design of overall system

The whole system contains two part--power supply part and current drive part, whose configuration is shown in Fig. 1. As we can see in Fig.1, we creatively design a two-stage soft-start circuit is designed to restrain the surge effectively when we turn-on and turn-off the power, another good point should point out is when the circuit is over-current, the current-limiting circuit will make the circuit work at the rated-current rather than cutting it down, so that the LD can work continuously and effectively.

### B. Design of power supply circuit

In this part, a high-voltage input of 220VAC is transformed into a low-voltage DC output to offer the power supply for the current drive circuit. As is shown in Fig. 2, 220VAC was transformed to a low voltage 12VDC by a transformer, bridge rectifier and filter network. Then it will be sent to the adjustable regulator LM338 to get a adjustable and stable output whose value is decided by formula(1).

$$V_o = \left(1 + \frac{R_{p1}}{R_2}\right) \times V_{ref} \quad (1)$$

In formula(1),  $V_{ref}$  is a constant value equals 1.25V.

As we can see in Fig. 2,  $F_u$  is a fuse that is used to protect

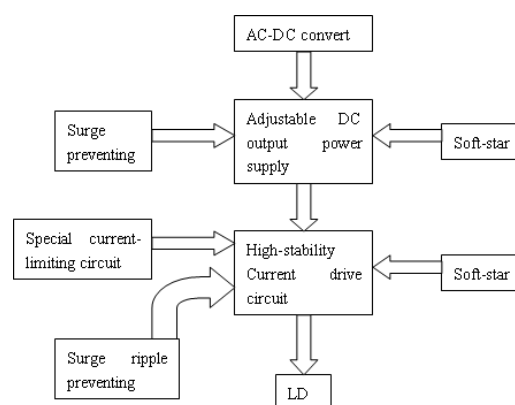


Figure 1. System design diagram

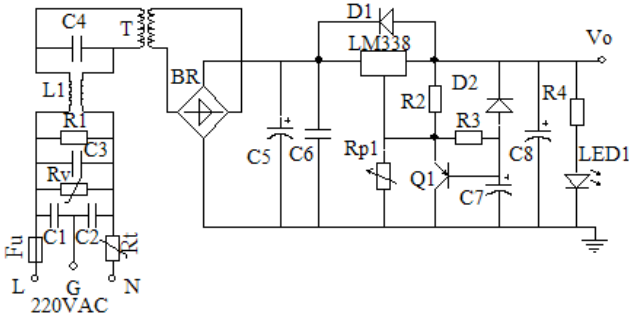


Figure 2. Adjustable power supply circuit

the circuit from over-current,  $R_t$  is a thermistor can limit the inrush current to avoid the fuse be damaged ahead of time,  $R_v$  is a voltage dependent resistor used to absorb the voltage surge.  $L_1$ ,  $C_1$ ,  $C_2$ ,  $C_3$ ,  $C_4$  and  $R_1$  compose a electromagnetic interference(EMI) filter;  $L_1$ ,  $C_1$  and  $C_2$  can restrain common mode interference while  $C_3$  and  $C_4$  are used to restrain series mode interference; moreover, when we cut down the power,  $C_3$  can discharge the electricity by  $R_1$ , so we can avoid the input port L and N charged with electron when we turn off the power.  $Q_1$ ,  $R_{p1}$ ,  $R_2$ ,  $R_3$ , and  $C_7$  compose a soft-start circuit. When we turn on the circuit, because the voltage of capacity  $C_7$  couldn't be changed suddenly, the voltage on base electrode of transistor  $Q_1$  is at a low potential, therefore  $Q_1$  will work in the saturated zoom, so  $R_{p1}$  will be a short circuit, the output  $V_o$  is about 1.5V; along with  $C_7$  is charged,  $Q_1$  will quit the saturated zoom gradually, the output  $V_o$  is increasing gradually, after the charging of  $C_7$  is completed,  $Q_1$  will work in the cut-off zoom, the output  $V_o$  will reach it's setting-value, the speed of slow start is decided by  $R_2$  and  $C_7$ . What's more,  $C_5$  can restrain the voltage ripple and  $C_6$  is used to eliminate the parasitic shock while  $C_8$  can improve the transient response of the regulated power supply; diodes  $D_1$  and  $D_2$  can protect the circuit when  $C_8$  leaks electron or the adjust port of LM338 is a short circuit. By the way, LED1 is a Indicator of the output voltage.

### C. Design of current drive circuit

The output  $V_o$  of the power supply part will be converted to a linear and adjustable current ranged from 0A to 2.5A. We have innovative idea of a two-stage soft-start circuit to protect the LD effectively when we turn-on or turn-off the power, we also design a special current-limiting circuit who can make the LD work at the rated-current when the circuit is over-current. What's more, the surge ripple preventing and filter circuit are design to improve the reliability, stability and security of the power supply.

#### 1) Current source ,soft-start,, surge ripple prevention and filter circuit

As is shown in Fig.3,  $L_2$ ,  $C_9$ ,  $C_{10}$  and  $L_3$ ,  $C_{13}$ ,  $C_{14}$  compose two  $\pi$  type filter circuit separately posited in the input port and output port of the circuit, they can effectively restrain the electromagnetic interference.

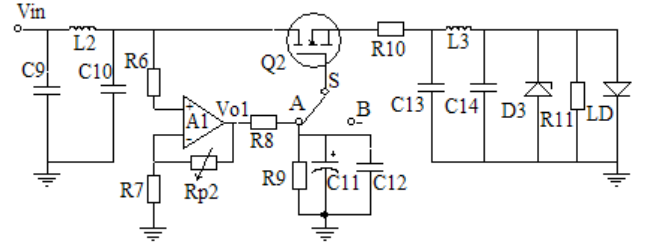


Figure 3. Current source and surge ripple prevention circuit

$R_6$ ,  $R_7$ ,  $R_{p2}$  and operational amplifier A1 compose a proportional amplifier with output:

$$V_{o1} = \left(1 + \frac{R_{p2}}{R_7}\right) \times V_{in} \quad (2)$$

Here  $V_{in}$  is the output  $V_o$  in Fig.2. As we all know, MOSFET is a voltage-controlled current component, we can control the drain current by adjust the grid voltage, therefore, MOSFET  $Q_2$  and proportional amplifier A1 compose an adjustable current source who can adjust the current through  $R_{p2}$ . As the LD is quite sensitive to the current, we need to increase the current on LD; this function is achieved by soft-start circuit. The soft-start function is completed by  $R_8$ ,  $R_9$ ,  $C_{11}$  and  $C_{12}$ , because the voltage on capacity can't be changed suddenly, the voltage on grid of  $Q_2$  can't be changed suddenly too, so the output current will increase or decrease gradually when we turn-on or turn-off the power. Zener diode  $D_3$  and  $R_{11}$  are used to absorb the surge current to protect the LD. S is a relay switch, usually S connect to A, but in protection mode, it will connect to B, this will be explained in the current-limiting circuit.

#### 2) Current-limiting circuit with special rated-current setting function

Although the soft-start circuit can eliminate the impact of high-frequency current, but it can't effectively prevent DC or low-frequency over-current, so current-limiting circuit must be designed to protect LD from overload[9]. The over-current protection function of traditional drive power[10,11] are too regular, they will cut down the output when it is over-current, therefore the LD couldn't work continuously if necessary. So we creatively design a special current-limiting circuit which can make the circuit work at a setting-current(usually the rated-current) when it is over-current.

As is shown in Fig.4,  $R_{10}$  is a precision resistor which is used to sample the drive current of the LD; A2 and A3 are both proportional amplifier, A4 is a subtractor, A2, A3 and A4 compose a current-voltage convert circuit who can convert the sampling current single to a voltage with high precision. As the input ports are non-inverting terminals, the input resistor is very high, A2 and A3 have the same characters, so they can restrain the temperature drift effectively, and the ability of anti-common-mode interference is very strong. We need to set  $R_{12}=R_{14}$ ,  $R_{15}=R_{16}=R_{17}=R_{18}$ , the output of A4 is described by formula(3).

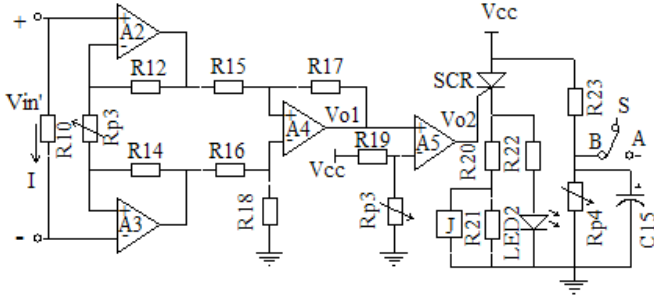


Figure4. Current-limiting circuit with special rated-current setting functions.

$$V_{o1} = \left(1 + \frac{2R_{12}}{R_p}\right) \times R_{10} \times I \quad (3)$$

The output  $V_{o1}$  of A4 is send to a comparator A5 to compare with a reference voltage which decided by the protection-current. The output  $V_{o2}$  of A5 will control the gate of a thyristor SCR. According to formula(3), when the LD is over-current, the output of A5 is a high voltage, so the SCR will turn on, the relay will connect B with S, then the circuit will work in protection mode. We can set the voltage of B by  $R_{p4}$  so we can set the output current at the rated-current. LED2 is used to show weather the circuit is work in protection mode.

### III. TEST OF EXPERIMENT AND RESULT ANALYS

#### A. Test of soft-start function

We connect the laser diode whose type is G0331 to the output port of drive power supply and we use an oscilloscope whose type is TDS2024 to record the result. It is clear from the Fig.5 that: After we turned on the drive power, LD work voltage tardily increases from zero to 2.3V by 4.5 seconds delay. Then we turn off the power, we can see the voltage decrease slowly too.

#### B. Test of surge prevention

We change the oscilloscope to AC mode and set the scanning period at 1.00 second; We adjust the drive current on the maximum value of the LD which is 0.8A, then we turn on

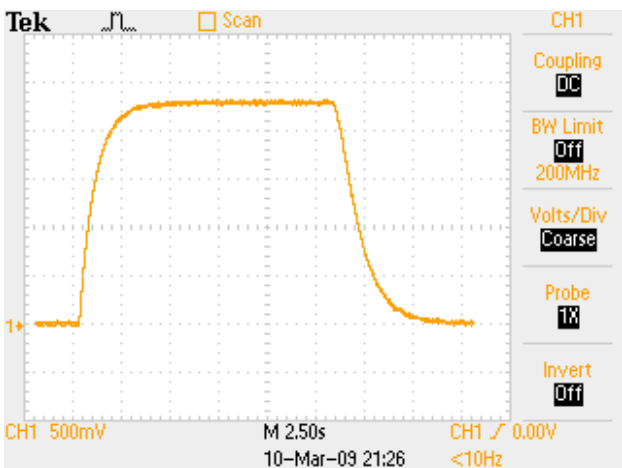


Figure 5. Curve of soft-start test..

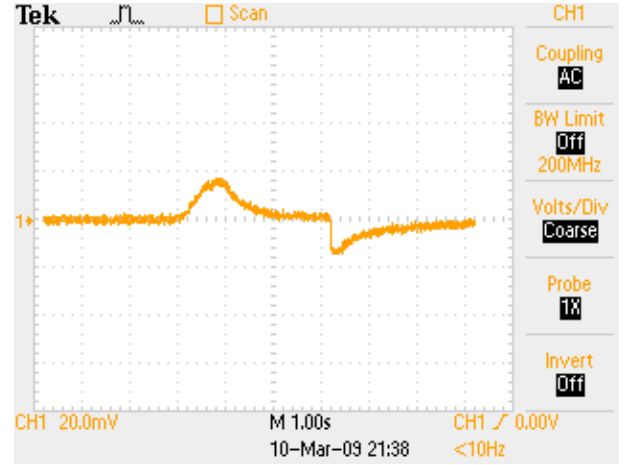


Figure 6. Curve of surge prevention test.

and turn off the drive power quickly, looking at the curve we record by the oscilloscope in Fig.6, we can see that the surge voltage is quite low, it is less than 20mV. We do the on-off convert time and time again, and the LD could work smoothly. It shows that this drive power supply has a high-efficient surge prevention function to enhance the stability and security of the drive system.

#### C. Test of output ripple

We adjust the drive current from the threshold 0.5A to the maximum value 0.8A, we found that the output voltage ripple is always less than 0.1%. Fig.7 shows the output voltage ripple when the drive current is 0.8A and the voltage across the LD is 2.3V. We can see the peak-peak value of the voltage ripple is 1.6mV, we do the test near to some computer powers and switch powers, and the ripple is still less than 0.1%. This means that the drive power has a high-efficient anti-interference function.

#### D. Test of over-current protection

Fig.8 can help us to understand the special over-current protection function. We remove the LD and connect the drive power to an equivalent load whose resistance is 2Ω. We set the protection current at 1.0A by  $R_{p3}$  in Fig.4; we also set the

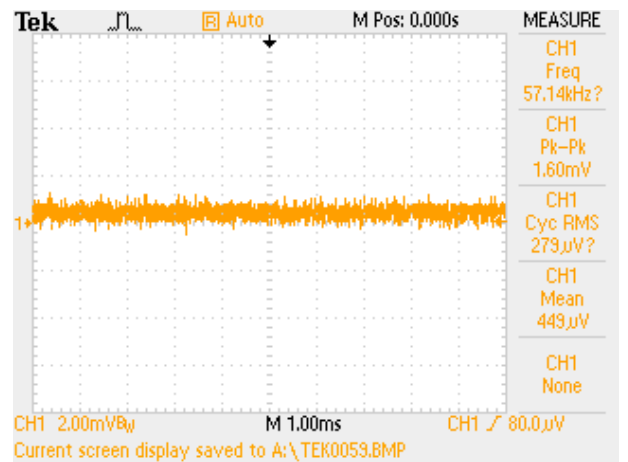


Figure 7. Test of output ripple voltage.

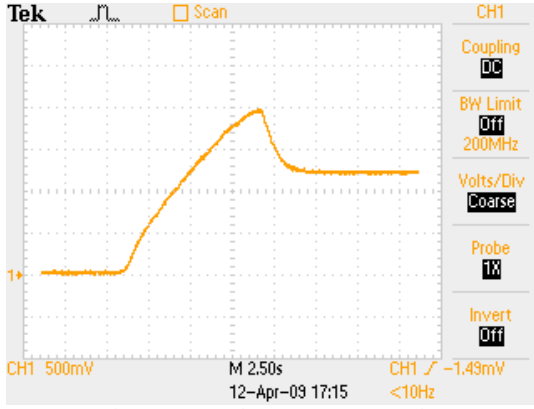


Figure 8. Test of over-current protection

rated-current at 0.6A by Rp4 in Fig.4, then we adjust the output current by Rp2 in Fig.3. As is implied in Fig.8, once the current exceed the protection value(1A), the output current will decrease to the setting value(0.6A) which is usually the rated-current.

#### E. Test of current-stability

An important parameter of LD drive power is current-stability. It is calculated by repeated measurement in certain time, it can be described by formula(4).

$$P = \frac{\text{average value} - \text{calibration value}}{\text{average value}} \quad (4)$$

The test data are recorded in table I. we can calculate that the average value is 800.233mV and the calibration value is 800mV. according to formula(4), we know the current-stability is 0.29%.

TABLE I. CURRENT-STABILITY MEASUREMENT

Time(min)	1	3	5	7	9	11
Current(mA)	800.00	800.08	800.16	800.22	800.14	800.20
Time(min)	13	15	17	19	21	23
Current(mA)	800.16	800.26	800.21	800.24	800.11	800.18
Time(min)	25	27	29	31	33	35
Current(mA)	800.09	800.28	800.14	800.19	800.15	800.39
Time(min)	37	39	41	43	45	47
Current(mA)	800.41	800.29	800.43	800.27	800.35	800.22
Time(min)	49	51	53	55	57	59
Current(mA)	800.16	800.24	800.53	800.28	800.25	800.36

#### F. Test of optical power

Joint the LD of type G0331 to the output port of the drive power and control the LD temperature to remains 15 °C, then we adjust the work current slowly, we got the output at the threshold current of 0.5A, and got the maximal optical power 700mW at the current of 0.8A. And the output wavelength is 635.1nm.

## IV. CONCLUSION

The LD drive power supply we design can effectively drive the laser diodes whose type is G0331. The EMI design and the surge prevention enormously improve the stability and security of the drive power supply whose current output is adjustable from 0A to 2.5A, the special over-current protection function make the LD work continuously if necessary. This new-style and high-stability LD drive power will found wide applications in the fields of teaching, scientific research and production.

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